# Power and Control Course Description

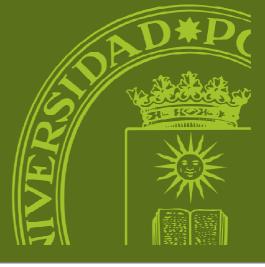


Departamento de Ingeniería Electrónica

Universidad Politécnica de Madrid

Máster Oficial en Ingeniería de Sistemas Electrónicos

www.die.upm.es



## Index

Power and Control	2
Objectives	2
Program	2
Bibliography	4
Teachers	4
Teaching Methodology	4
Evaluation	4
Contact	5

# **Power and Control**

Semester: 2

Number of credits: 4 (3 theory 1 practical)

Type: optional

## **Objectives**

The aim of the course is that students gain knowledge about power electronics and process control. Regarding power electronics would be treated aspects of power electronic devices, linear regulators and switching regulators. In relation to process control, teaching objectives include mathematical aspects, analysis in time and frequency, and design of compensators and controllers.

### Program

Topic 1: power electronic components (4 hours)

- Power diodes
- Power Bipolar Transistor
- Power MOSFET Transistor
- Comparison of power transistors.
- Drivers
- Exercises

Topic 2: Voltage Linear Regulators (7 hours)

2

- Structure of a linear power supply.
- Drive parameters.
- Linear Regulators.
- Protection circuits.
- Integrated Regulators.
- practical circuits.
- Exercises

Thread 3: switched regulators (8 hours).

- Principle of operation. Comparison with linear regulators.
- Basic topologies of converters.
  - Reducing Converter. Analysis of continuous mode operation. Waveforms.
  - Up converter. Analysis of continuous mode operation. Waveforms.
  - Converter or inverter. Analysis of continuous mode operation. Waveforms.
- Voltage Mode PWM Control.
- Exercises.

Lab: Implementing a switching regulator (3 hours).

Topic 4: Introduction to automatic process control and dynamic systems modeling (5 hours)

- Closed-loop control vs. open loop control
- Linear Systems. Invariant linear systems. Laplace Transform.
- Block Diagrams.
- Simulation with Octave or Matlab
- Exercises

Lab: characterization of a mechanical system (DC motor) and speed control open loop power transistors and pulse width modulation (PWM) (2 hours)

Topic 5: Analysis of Control Systems (6 hours)

- Timing and frequency response. Analysis and simulation of first and second order
- Control actions: on / off, proportional, integral, derivative
  - Steady state error (steady-state)
  - Loads and disturbances Analysis
- Stability analysis: root locus. Nyquist criterion. Phase margin and gain margin. Simulations.
- Exercises

Topic 6: Design of compensators and controllers (4 hours)

- Phase lead compensators
- Phase-lag compensators
- Adjust PID controller (Ziegler-Nichols)
- Exercises

Lab: Implementing a control system: control system of a DC motor using PWM, testing various control algorithms (3 hours)

## **Bibliography**

Main

- DESIGN WITH OPERATIONAL AMPLIFIERS AND ANALOG INTEGRATED CIRCUITS. (Third Edition). Sergio Franco. Editorial: McGraw-Hill Int. 2002.
- MODERN CONTROL ENGINEERING, K. Ogata, Ed. Prentice Hall 2012

Complementary

- ELECTRÓNICA DE POTENCIA. Daniel W. Hart. Editorial: Prentice-Hall. 2001.
- POWER ELECTRONICS: CONVERTERS, APPLICATIONS AND DESIGN (2nd Edition). Ned Mohan, Tore M. Undeland, William P. Robbins. Editorial: John Wiley. 1995.
- TECNOLOGÍA MICROELECTRÓNICA. Ramiro Alvarez Santos. Editorial: Ciencia 3. 1988.

## Teachers

Coordinator: Juan Manuel Montero Martínez

Teachers: Rubén San Segundo, Pedro Malagón, Luis F. D'Haro

# **Teaching Methodology**

For the development of the course will be taught participatory lectures (with simulations in Octave / Matlab), discussion sessions and practical problem solving.

In parallel, several practical works will be proposed.

### **Evaluation**

The evaluation focuses on two main aspects:

1. A practical mainly written exam, where students have to solve exercises and practical cases, similar to those seen in class.

2. Two practical works. These works will be focused on the implementation of a switching regulator and a control system of a DC motor



4

The final grade will be: 70% written examination and 30% practical work

### Contact

Juan Manuel Montero: desk B-110 (<u>Juancho@die.upm.es</u>) Rubén San Segundo: desk B-109 (<u>lápiz@die.upm.es</u>) Pedro Malagón: desk B-113 (<u>malagon@die.upm.es</u>) Luis Fernándo D'Haro: desk B-108 (<u>lfdharo@die.upm.es</u>) Moodle:

